

PATENT APPLICATION
042390.P5271

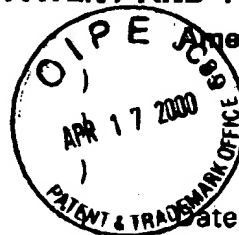
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Rosenberg et al.

Application No.: 08/993,104

Filed: December 18, 1997



Amendment under 37 CFR 1.116

Expedited Procedure

Group Art Unit 2774

Date: April 4, 2000

Examiner: F. Nguyen

Art Unit: 2774

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C 2700 MAIL ROOM

For: **VOLTAGE SIGNAL MODULATION SCHEME**

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING
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INTEL CORPORATION

Name of Assignee

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DATE

RESPONSE TO FINAL OFFICE ACTION

HONORABLE ASSISTANT COMMISSIONER FOR PATENTS,
Washington, D.C. 20231

SIR:

In response to the Final Office Action mailed December 28, 1999, please
reconsider the above-identified patent application in view of the remarks below.
Applicants would like to thank the Examiner for his efforts in reviewing the
amendments and comments provided by Applicants in the response mailed
November 2, 1999.

Applicants believe the following is a complete response to the issues raised
by the Examiner and that the following comments place the application into
condition for allowance.